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Design of Analogue CMOS VLSI MEMS Sensor

*A dissertation presented in partial fulfillment
of the requirements for the degree of*

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by

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Abstract

There is an increasing demand of a highly sensitive and reliable pressure micro-sensor system, for implantable and non-implantable medical applications. The prerequisite of a miniaturized device for minimally invasive procedures, posed greater challenges in the complex integrated design of micro-system. Micro-sensor system designs in the recent advanced CMOS technologies are explored in this work for effective system miniaturization and improved performance. The material choices and geometry designs, which significantly influence the sensitivity and dynamic range of the micro-scale sensor devices, are well addressed. Co-integrations of MEMS devices with signal conditioning circuits that effectively reduce the parasitic effect are also performed for enhancing the overall system performance. In addition, system reliability is also improved with on-chip metal interconnections. The employed process technologies to a greater extent contributed to the high yield for these low cost micro-sensor systems.

This research focuses on the design of integrated CMOS MEMS capacitive pressure sensors for diverse bio-medical applications. Two monolithically integrated capacitive pressure micro-sensor systems are designed, fabricated and experimentally verified. A novel micro-electro-mechanical capacitive pressure sensor in SiGeMEMS process, vertically integrated on top of a 0.18 μm TSMC CMOS processed die is proposed. The perforated elliptic diaphragm, which is edge clamped at the semi-major axis is developed using poly-SiGe material. High performance on-chip CMOS conditioning circuits are also designed to achieve better overall sensitivity. Experimental results indicate a high sensitivity of around 0.12 mV/hPa along with a non-linearity of around 1% for the full scale range of applied pressure load. The L-clamp spring anchored diaphragm provided a wide dynamic range of around 900 hPa. Another integrated capacitive pressure micro-system, developed using the advanced standard IBM CMOS process in two geometrical designs is also proposed. A step-sided elliptic diaphragm that overcomes the CMOS process limitations is fabricated to achieve regulated membrane deflections and improved sensitivity. A foundry compatible post-process technique, for a lateral release length of 125 μm is also performed successfully on the 130 nm CMOS platform. A current cross mirroring technique is utilized to enhance the transconductance of an on-chip operational amplifier to achieve a high single stage gain. Sensitivities of the fluorosilicate sealed absolute pressure sensors were measured to be 0.07 mV/Pa and 0.05 mV/Pa for the elliptic and rectangular element, respectively. In addition, the linear capacitive transduction dynamic range was found to be 0.32 pF and 0.23 pF, respectively, for the elliptic and rectangular element.

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List of Acronyms and Abbreviations

AFM	Atomic Force Microscope
APM	Ammonia hydroxide-hydrogen Peroxide
ADC	Analog to Digital Converter
BEOL	Back End Of Line
BSIM	Berkeley Short-channel IGFET Model
CAD	Computer Aided Design
CHS	Chopper Stabilization
CMFB	Common Mode Feed Back
CMRR	Common Mode Rejection Ratio
CMOS	Complementary Metal Oxide Semiconductor
DRC	Design Rule Check
DRIE	Deep Reactive Ion Etching
DIP	Dual line in package
DUT	Device Under Test
DWW	Direct on wafer writing
EDA	Electronic Design Automation
ESD	Electrostatic Discharge
FC opamp	Folded Cascode Operational Amplifier
FC out+	Positive going output voltage of folded cascode opamp
FC out-	Negative going output voltage of folded cascode opamp
FC _{out} _{diff}	Differential output voltage of folded cascode opamp
FEA	Finite Element Analysis
FEOL	Front End Of Line
GBW	Gain Bandwidth Product
GUI	Graphical User Interface
HPM	Hydrochloric acid-hydrogen peroxide
IC	Integrated Circuit
ICMR	Input Common Mode Range
ICP	Inductively Coupled Plasma
IEEE	Institute of Electrical and Electronics Engineers Inc.
LPF	Low Pass Filter
LPCVD	Low Pressure Chemical Vapor Deposition

Lp_OUT+	Positive going output voltage of LPF
Lp_OUT-	Negative going output voltage of LPF
M-Chopper	Modified Chopper Stabilization
MEMS	Micro Electromechanical System
MOSFET	Metal Oxide Semiconductor Field Effect Transistor
MST	Minimum Settling Time
MUMPS	Multi-User MEMS Processes
OTA	Operational Transconductance Amplifier
PMMA	Poly methyl methacrylate
PDMS	Polydimethylsiloxane
PGA	Pin Grid Array
PSRR	Power Supply Rejection Ratio
RF	Radio Frequency
RFC	Recycled Folded Cascode opamp
RIE	Reactive Ion Etching
Sensor _{out+}	Positive going output voltage of the sensor
Sensor _{out-}	Negative going output voltage of the sensor
SNR	Signal to Noise ratio
SDL	Schematic Driven Layout
SIP	System in package
THD	Total Harmonic Distortion
UGB	Unity Gain Bandwidth
VL_OUT _{diff}	Differential Output voltage of OTA
VM	Virtual Metrology
ZIF	Zero Insertion Force

List of Symbols

C	Capacitance	Farad
ϵ_r	Relative Permittivity	Farad per meter
ϵ_o	Permittivity of free space	Farad per meter
A	Area of the capacitor plate	Meter Square
λ	Channel length modulation	Micrometer
μ_n	Electron mobility	Meter square/Volts seconds
μm	Micrometer	
ϵ_{xx}	Normal strain in x direction	
ϵ_{yy}	Normal strain in y direction	
ϵ_{xy}	Shear strain	
w	Deflection of the plate	Micrometer
σ_{xx}	Normal stress in x direction	Newton/meter square
σ_{yy}	Normal stress in y direction	Newton/meter square
τ_{xy}	Shear stress	Pascal
E	Young's Modulus	Newton/meter square
ν	Poisson's ratio	
P_o	Applied Pressure	Hecto-Pascal
D	Flexural rigidity	Newton meter square
M_x	Bending moments in x direction	Newton Meters
M_y	Bending moments in y direction	Newton Meters
M_{xy}	Shear moments in x direction	Newton Meters
Pa	Pascal	Newton/meter square
g_m	Transconductance	Ampere/microvolt
k	Boltzmann's constant	Joules/Kelvin
T	Absolute temperature	Kelvin
R	Resistance	Ohms
B	Noise bandwidth	Hertz
W	Channel width of the MOSFET	Micrometer
L	Channel length of the MOSFET	Micrometer
I_D	DC Drain current of MOSFET	Amperes
i_d	Small signal drain current of MOSFET	Amperes
C_{ox}	Gate oxide capacitance	Farad
V_{cm}	Common mode voltage	Volts
A_{FC}	Open loop gain	Decibels
Q	Quality factor	